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Application Number	10529688
Filing Date	2005-03-29
First Named Inventor	Jozef R. M. Bergervoet
Art Unit	2611
Examiner Name	Jayanti K. PATEL
Attorney Docket Number	NL020959

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/VP/	1	ROBERT C. FRYE: Integration and Electrical Isolation in CMOS Mixed-Signal Wireless Chips, Vol. 89, no. 4, April 2001, pages 444-455, IEEE	<input type="checkbox"/>
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